EXPRESS MAIL LABEL NO. EV314046062US DATED: 16 JULY 2003

ATTY DOCKET: MXIC 1532-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Inventor: Chih Yuan Huang et al.	Group Art Unit: unknown
Application No.:	Examiner: unknown
Filed: 16 July 2003 (herewith)	
Title: Method for Forming Silicon Film with Changing Grain Size by Thermal Process	Customer No. 22470

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56.

Enclosed with this statement are the following:

- ✓ Form PTO-SB/08. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. §609.
- ✓ A copy of each cited document as required by 37 C.F.R. §1.98.

This statement should be considered because:

This statement qualifies under 37 C.F.R. §1.97, <u>subsection (b)</u> because it is being filed within three months of the filing date of an application other than a continued prosecution application under § 1.53(d);

The Commissioner is hereby authorized to charge underpayment of any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (MXIC 1532-1). A duplicate copy of this authorization is enclosed.

Respectfully submitted,

HAYNES BEFFEL & WOLFELD LLP

Warren S. Wolfeld, Reg. No. 31,454

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Date: 7/16/2003

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			LOGUET	Application Number		
INFC	PRMATIO	N DISC	LOSURE	Filing Date	16 July 2003 (herewith)	
STA	TEMENT	BY AP	PLICANT	First Named Inventor	Chih Yuan Huang et al.	
				Group Art Unit		
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Sheet	1	of	3	Attorney Docket Number	MXIC 1532-1	

	U.S. PATENT DOCUMENTS								
Examiner Initials*	Cite No.1	U.S. Patent Document Number Kind Code² (If known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear				
	A1	4,963,506	Liaw et al.	10-16-1990					
	A2	5,751,637	Chen et al.	05-12-1998					
	A3	5,753,134	Biebl	05/19/1998					
,	A4	5,874,129	Beinglass et al.	02-23-1999	·				
	A5	6,268,068	Heuer et al.	07-31-2001					
	A6	6,373,092	Okumoto	04-16-2002					
_	A7	10/180,168	Luoh	06-26-2002	(filing date)				
	A8								
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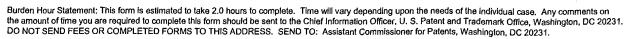
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				Application Number	
INF	ORMATION	1 L	ISCLOSURE	Filing Date	16 July 2003 (herewith)
STA	TEMENT I	BY	APPLICANT	First Named Inventor	Chih Yuan Huang et al.
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Sheet	3	of	3	Attorney Docket Number	MXIC 1532-1

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	C12	DORSCH, JEFF, "Applied Debuts RTP System, Company also adds CMP Process, defect detection system" Electronic News (09/20/1999), 3 pages				
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Substitute for form 1449B/PTO				Complet if Known		
				Application Number		
INFORMATION DISCLOSURE				Filing Date	16 July 2003 (herewith)	
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Sheet	2	of	3	Attorney Docket Number	MXIC 1532-1	

		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	т-
Examiner Initials*	Cite No.1	Include name of the author (In CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	C1	KAHLER, U., et al. "Size related properties of silicon nanoparticles from decomposition of SiOx" http://www.mpi-halle.mpg.de/annual_reports/2000.pdf/jb_results_14.pdf, 2 pages.	/
	C2	"Polysilicon: The Successor Technology," http://www.wtec.org/loyola/dsply_jp/c5_s5.htm (June 1992), 3 pages	7
	C3	Polysilicon TFT, The Future is clear with Toshiba, http://www.toshiba-europe.com/computers/tnt/tft/ (2002), 1 page	1
	C4	Higher Resolution, The Future is clear with Toshiba, http://www.toshiba-europe.com/computers/tnt/tft/resolution.htm (2002), 1 page	1
	C5	Polysilicon Explained, The future is clear with Toshiba, http://www.toshiba-europe.com/computers/tnt/tft/explained.htm (2002), 1 pate	/
	C6	"Flash Memory Technology" Chapter 10, Integrated Circuit Engineering Corporation, http://smithsonianchips.si.edu/ice/cd/MEMORY97/SEC10.PDF (1997), pp. 10-1 through 10-16.	/
·-·	C7	WILSON, RON, "Flash gets a quantum makeover at IEDM," EETIMES (12/13/2002), 4 pages.	/
	C8	"The K-Input Floating-Gate MOS (FGMOS) Transistor" http://people.ece.cornell.edu/minch/talks/FGMOSTdevicelayout.pdf, 22 pages	
	C9	DIPERT, BRIAN, "Exotic Memories, diverse approaches" www.ednmag.com (04/26/2001), 9 pages	
	C10	LAMMERS, DAVID, "Chip makers bang away at metal gates" EETIMES (11/26/2002), 3 pages	1
	C11	"Characterizing Very Thin ONO Gates with Laser Spectroscopic Ellipsometry" Rudolph Technologies, Inc. Application Reports (2001) http://www.rudolphtech.com/reports/VeryThinONOGates.htm, 3 pages	

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